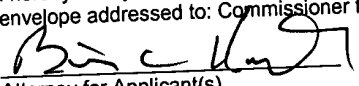


CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on September 30, 2003.


Attorney for Applicant(s)

PATENT APPLICATION

Docket No.:TUC920030095US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Gary W. Barchelor et al.

Serial No.: Not yet assigned

Filing Date: September 30, 2003

For: SYSTEM, APPARATUS, AND METHOD FOR
LIMITING NON-VOLITILE MEMORY

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) Group Art
) Unit:
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INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This Information Disclosure Statement discloses information which has come to the attention of applicant and his attorneys and is being submitted so as to comply with the duty of disclosure set forth in 37 C.F.R. § 1.56. In accordance with 37 C.F.R. § 1.97(b), this Statement is being filed within three (3) months of the filing date of the above-identified application or before the mailing date of a first Action on the merits.

Neither applicant nor his attorneys make any representation that any information disclosed herein may be "prior art" within the meaning of that term under 35 U.S.C. §§ 102 or 103. Moreover, pursuant to 37 C.F.R. § 1.97, the filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made or as an admission that the

information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

In accordance with 37 C.F.R. § 1.98, this Information Disclosure Statement includes and is accompanied by:

1. A completed copy of Form PTO-1449 listing the patents, publications and other information being submitted for consideration; and
2. A legible copy of each patent, publication and other item of information in written form listed on the enclosed Form PTO-1449.

NON-ENGLISH INFORMATION

Pursuant to 37 C.F.R. § 1.98, following is a concise explanation of the relevance (as it is presently understood by the individual designated in 37 C.F.R. § 1.56(c) most knowledgeable about the content of the information), of each listed patent, publication or other information that is not in the English language.

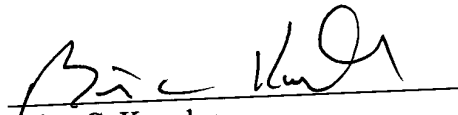
APPLICATION NO. JP 63155213

Abstract:

Purpose: To improve the operating rate of a device by placing a semiconductor memory in a stand-by state immediately at the time of power failure switching unit battery capacity reaches a certain value without saving the contents of the semiconductor memory in a nonvolatile memory.

Constitution: The semiconductor disk device is equipped with a rectifying circuit 31, a power circuit 32, a power failure detecting circuit 33, a power source control circuit 34, a power switch circuit 35, a battery capacity monitoring circuit 36, a memory control circuit 37, a semiconductor memory 38, and MD (microdisk unit) 40 as the nonvolatile storage unit, and a data saving control circuit 39. Then, when a power failure occurs, DC electric power from a battery is supplied to the power circuit 32 through a DC switch circuit 35. In this case, when a power failure detection signal is received, the operation state of the semiconductor memory 38 is switched to the stand-by state. Then, when the remaining capacity of the battery decreases to a specific value, the monitoring circuit 36 sends a signal PF2 through a power control circuit 34, the memory control circuit 32 saves the storage contents of the memory 38 on the MD 40 and the power source is turned off.

Respectfully submitted,



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Date: September 30, 2003

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FORM PTO-1449 LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (use several sheets if necessary)	SERIAL NO. Not yet assigned	ATTORNEY DOCKET NO. TUC920030095US1
	FILING DATE September 30, 2003	GROUP ART UNIT
	APPLICANT(S): Gary W. Batchelor et al.	

REFERENCE DESIGNATION**U.S. PATENT DOCUMENTS**

EXAMINE R INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS/ SUBCLASS	FILING DATE
	A1	5,625,237	04/29/1997	Saeki et al.	307/48	06/29/1995
	A2	5,822,600	10/13/1998	Hallowell et al.	395/750.08	07/19/1996
	A3	6,304,981	10/16/2001	Spears et al.	714/24	10/19/1998
	A4	2003/0046503	03/06/2003	Park	711/162	09/04/2002

FOREIGN PATENT DOCUMENTS

EXAMINE R INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION	
						YES	NO
	A5	JP63155213	06/28/88	Japan	G06F 1/00		

NON-PATENT DOCUMENTS

EXAMINE R INITIAL		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).